Computer Science

Prospectus No. 20111216

संत गाडगे बाबा अमरावती विद्यापीठ SANT GADGE BABA AMRAVATI UNIVERSITY

विज्ञान विद्याशाखा (FACULTY OF SCIENCE)

PROSPECTUS

PRESCRIBED FOR

M.Sc. Semester I & III W-2010 and Semester II & IV S-2011

N Semester II & IV S-2011

COMPUTER SCIENCE



2010

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PUBLISHED BY **Dineshkumar Joshi**Registrar Sant Gadge Baba

Amravati University Amravati-444602

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SANT GADGE BABA AMRAVATI UNIVERSITY SPECIAL NOTE FOR INFORMATION OF THE STUDENTS

- (1) Notwithstanding anything to the contrary, it is notified for general information and guidance of all concerned that a person, who has passed the qualifying examination and is eligible for admission only to the corresponding next higher examination as an ex-student or an external candidate, shall be examined in accordance with the syllabus of such next higher examination in force at the time of such examination in such subjects papers or combination of papers in which students from University Departments or Colleges are to be examined by the University.
- (2) Be it known to all the students desirous to take examination/s for which this prospectus has been prescribed should, if found necessary for any other information regarding examinations etc., refer the University Ordinance Booklet the various conditions/provisions pertaining to examination as prescribed in the following Ordinances.

Ordinance No. 1 : Enrolment of Students.

Ordinance No. 2 : Admission of Students

Ordinance No. 4 : National cadet corps

Ordinance No. 6 : Examinations in General (relevent extracts)

Ordinance No. 18/2001: An Ordinance to provide grace marks for passing in a Head of passing and Inprovement of Division (Higher Class) and getting Distinction in the subject and condonation of defficiency of marks in a subject in all the faculties prescribed by the

Ordinance No. 9 : Conduct of Examinations (relevent extracts)
Ordinance No. 10 : Providing for Exemptions and Compartments

Statute NO.18, Ordinance 2001

Ordinance No. 19 : Admission of Candidates to Degrees.

Ordinance No. 109 : Recording of a change of name of a
University student in the records of the
University.

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Ordinance No. 6/2008 : For improvement of Division/Grade.

Ordinance No.19/2001 : An Ordinance for Central Assessment

Programme, Scheme of Evaluation and

Moderation of answerbooks and

preparation of results of the examinations,

conducted by the University, Ordinance

Dineshkumar Joshi

Registrar Sant Gadge Baba Amravati University.

PATTERN OF QUESTION PAPER ON THE UNIT SYSTEM

The pattern of question paper as per unit system will be broadly based on the following pattern

- (1) Syllabus has been divided into units equal to the number of question to be answered in the paper. On each unit there will be a question either a long answer type or a short answer type.
- (2) Number of question will be in accordance with the unit prescribed in the syllabi for each paper i.e. there will be one question on each unit.
- (3) For every question long answer type or short answer type there will be an alternative choice from the same unit. However, there will be no internal choice in a question.
- (4) Division of marks between long answer and short answer type question will be in the ratio of 40 and 60
- (5) Each short answer type question shall contain 4 to 8 short sub question with no internal choice.

M.Sc.PART-I SEMESTER I (EXAMINATION)

Syllabus prescribed for

M.Sc.Part-I & Part-II Semester I & II(Computer Science)

1MCS1 DIGITAL SYSTEMS & MICROPROCESSORS

Unit-I : Introduction to logic families :

TTL, ECL, MOS, CMOS etc. and their characteristics, tristate, flip-flops, RS, JK, JKMS, D, T, IC series for gates and flip-flops.

Combinational Logic Design:

Standard forms of logical functions, SOP, POS, minterms, maxforms, K-map, IC series for combinational logic.

- Unit-II: Multiplexers, demultiplixers, decoders, encoders, combinational logic design, adder & their use as subtractor, BCD arithmatic, ALU, Digital Comparators, parity generator / checkers, parity encoders / decoders, IC series for all these devices.
- Unit-III: Seqential Logic Design: Resisters, application of shift register, counters, asynchronous & synchronous counters, Design of counters, speed, up-down counters, applications of counters, introduction to various counters and shift register ICs, digital memory unit, types.
- Unit-IV: Register Transfer Logic: Introduction, inter register transfer, arithmatic, logic and shift micro operations, conditional control statements, overflow, arithmatic shifts, fixed binary data, decimal data, floating point data, nonnumeric data, instruction codes, design of computer.
- Unit-V: Processor Organisation: (8086): Register organisation, Architectures, signals, memory organisation, generle bus operation, I/O addressing, special processor activities, minimum and maximum mode, instruction formats, addressing modes, important instructions, assembler directives and operators.

Unit-VI: Special Artchitectural features and programming:

Stack, structure, interrupts, ISR, NMI & INTR, interrupt programming.

Interfacing memories, I/O ports, ADC, DAC

BOOKS

- 1) Digital Logic and Computer Design M.Morris Mano Pearson.
- 2) Digital Integrated Electronics Taub & Sehilling
- 3) Modern Digital Electronics R.P.Jain
- 4) Digital Fundamentals 3/e (Indian Adaptation) Floyd & Jain Pearson.

- Digital Design Wakerly Pearson
- 799 Advance Microprocessors and Peripherals - Ray & Bhurchandi
- Microprocessors and Interfacing: D.V.Hall (TMH)
- 9 8 Microprocessors - M.Rafiquezaman (PHI)
- Microprocessor based system Design Ghoshal (M)
- Microprocessor Architecture and Programming R.S.Goonkar (PRI)

1MCS2 DATA STRUCTURES AND ALGORITHMS (C++

Arrays, Records, Stacks and Queues, linked list:

Algorithms and operations on these data structures.

Unit-II : Tree, tree traversals, threaded binary tree, Height balanced minimum spanning tree. tree, graph, multidimensional array, graph traversals

algorithms, recursive quick sort, sequential searching, binary sort, selection sort, shell sort, heap sort, merge sort, Searching & sorting techniques: Bubble sorting, insertion search, hashing, indexed search techniques.

Unit-IV: sequential files, hash files, file security. File Organisation: Sequential File organisation, index

Unit-V : gready methods, optimal storage on type, job requesting Introduction algorithms and their analysis, divide & conquer & bounds, lower bound theory. back tracking, 8 queens problem, knapsack problem, brancl

problems, NP hard code generations. NPhard and NP complete problems: Basic concepts, Cook's theorem, NP hard graph problems, NP hard scehduling

Books:

- Data Structures and Algorithms in C++ B.R. Weiss Pearson
- Kamthane Int. to Data structure in c Pearson
- Computer Algorithms Baase & Gelder Pearson
- Data Structure using Java, L/C : Langman Pearson
- Introduction to Data Structure Trenble, Sorenson.
- Introduction to Data Structure Bhagat Singh, Nops.
- Fundamentals of Computer Algorithm: Horowitz & Sahani.
- Design & Analysis of Comp. Aho & Ullamann- Pearson
- Algorithms with C K. London (SPD)
- Levitin Int.to Design & Analysis of Algorithms Pearson
- Bandopadhyay Data Structuring C Pearson
- Aho Data Structures & Algo Pearson

1MCS3 **OBJECT ORIENTED PROGRAMMING**

Unit-I : programming, Internet & WWW, basics of Java Introduction: History of C++ and Java, Structured environment.

concepts, arithmetic, decision making. Java Application: Introduction, simple program, memory

Java Applets: Introduction, Sample applets example.

Unit-II : Controlled Structures: if, if-else, while, for, switch, do break, continue.

Methods: Introduction, definition, math, Java application package, scope rules, recursion, over loading, abstraction

Unit-III : constructors, methods. Arrays: Introduction, declarations, allocations, parameters multidimentional arrays. String & characters: Class passing arrays to methods, sorting and searching arrays

Unit-IV: **Object Oriented Programming:**

visuasability, instances, reference, finalisers, abstraction controlling, access, creating package, constructors Object based programming: Introduction, class scope information hiding.

Unit-V : catching. : Introduction, error handling, techniques, throwing Exception Handling & Multithreading: Exception handling

Multithreading: Thread Class & Method, thread state priority, scheduling, synchronisation, groups

Unit-VI : Graphics & Multimedia:

Graphics: 2D Graphics, Java 2D API, shapes, GUI textarea, panel, slider, menus box. Introduction, suins overview, event handling, creating

Introduction to Multimedia using Java

Books:

- Java How to Program : Dietel & Dietel Pearson
- Core Java Vol.II: Advance Features 5th Ed: Hortsman Pearson
- Inside Servlets D.R.Callaway Pearson
- Java A primer Balguruswamy (TMH)
- C++ & Java Sananta (PHI)
- Java Programming Language 3rd Ed. Arnold, Gusling, Holmes
- Java 2 Essentials Horstman (W)
- Java 2 From Scratch Itecines (Que)
- Designing Java 2 : I.Hortou (SPD)
- Cadenhead Java 2 in 21 days Pearson

DIGITAL COMMUNICATION & NETWORKING

UNITI : Introduction to data communication N/W application, telephone communication hardware.

UNIT-II: Data communication Hardware, N/W architecture, Hosts, clients circuits, Data communication devices, data transmission.

UNIT-III: Data link layer: Access control, Error control, protocols trib N/W layer: Topology, Routing, standards, protocols,

UNIT-IV: LAN & WAN: Introduction, components, IEEE

PSN, ATM, interfacing networks WAN : DDD, AT&T, WAIS, Dedicated ckt services, ISDN

UNIT-V: Back bone networks, Network design & implementation N/ W management security.

UNIT-VI: Novelle Netware: Introduction, Server, setting up server netware commands.

Books Recommended:

- Business Data Communications & Networking 5th edition Jerry Fitzgerald, Alan Dennis.(WE)
- 7 Data Communications, Communications and Open System - 2nd Edition, Fred Halsoll - Pearson
- Computer Networking Kurose & Ross Pearson
- <u>ω</u> 4 c Computer Network and Internet - 4th Edition - DE Comer - Pearson
- Communications, Systems and Networks 2nd Ed. Ray Horok
- 9 Sklar - Digital Comm. - Pearson

1MCS5 SYSTEM ANALYSIS, DESIGN AND SOFTWARE ENGINEERING

SDLC: Goals, Computer based business system life cycle DFD, DFDS with case, structured methodology.

Unit-II : reports, input design, file design, database design, network System Design: Output design, formatting and designing System Analysis: Goals and overviews, fact finding, interviewing, review, assignment, prototyping and 4GL

Unit-III: Software Project Management: Concepts, Software metrics management issues.

design, s/w design, implementation, maintainance and

configuration management models, risk management, project scheduling and tracking Software Project Planning, Software Project estimation

Unit-IV: software reliability, ISO standards Software Maintenance: Reverse Engineering, SQA

product Engineering, modelling. Software Requirements and Analysis: System Engineering

Unit-V : documentation design. System Design: Effective design, methods, interface design

metrics, test reports. Software testing: Methods, Strategies, Art of designing

Unit-VI: Software Engienering: Overview, reverse engineering. reuse, CASE tools. forward engineering, metrics for maintainance, Software

- System Analysis and Design Edwards
- System Analysis & Design Don Yates (M)
- Fundamentals of System Analysis & Design J.F.Gerald
- Software Engineering with Java S.R.Schach (TMH)
- Software Engineering Press Man (TMH)
- Sommerville Software Engg. 7th ed. Pearson
- Booch Object Oriented Analysis & Design Pearson
- Kendoll Systems Analysis & Design Pearson

1MCS 6 COMPUTER LAB-I

Practicles based on subjects 1,4,5.

1 MCS7 **COMPUTERLAB-II**

Practicles based on subjects 2, 3

"Distribution of Marks for Computer Lab.-I & Lab.-II

- $\stackrel{\mathcal{A}}{=}$ Each student shall perform two practicals
- Questions slip for each examinee, shall be based to anser book

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Marks should be given on the basis of the following break up.

IV) Record Book III) Viva-Voce (Each practical 10 marks): II) Practical-II Practical-I 06 Marks 20 Marks 12 Marks 12 Marks

Total	
50 Marks"	

2MCS1 SYSTEMSOFTWARE AND OPERATING SYSTEM SEMESTER-II

UnitI Introduction, Basic Assembler functions, One-pass relocation, linkage aditors, dynamic linking. assembler, multi-pass assembler, loaders & linkers,

Compilers, phases of compilation, lexical analysis, parsing compilation of expression, control structures, code optımızatıon.

of processes, process scheduling algorithms, file Functions of OS, types of OS, process management, states management, space allocation techniques, directory types and structures.

Memory Management, partitious, paging, segmentation virtual memory management, demand paging, page replacement algorithms, thrashing.

Unit-V : Space Allocation Techniques: Secondary storage, disk scheduling algorithms, concurrency control, deadlocks.

Unit-VI: Case Study: Unix Operating System & Windows NT O.S Process Management, memory management, I/O m'ment

- "S.S. & O.S." D.M.Dhamdhere (TMH)
- Operating System 3 / e : Nutt Pearson
- "Operating System Concepts" Silbershatz (Addision Wesley)
- 200400 "Systems Software", Leland Beck (Pearson Education)
 - "Operating Systems" William Stallings. (PE) 4th ed
 - "Operating Systems" A.S.Godbole TMH
 - "Operating Systems" Crowley TMH
- "System Programming" Donovan TMH.
- "Modern Operating Systems" Tenenenbaum Pearson Education.
- Deitel Operating Ssytem Pearson

2MCS2 WINDOWS PROGRAMMING

Unit-I : CWnd, C Main Frame classes, handling windows, messages Introduction to MFC: MFC class hierarchy, Cwin ALP,

C point, C size, C Rect, C Array, and C List. Document / view architecture : C Document and C view, C++ templete classes review, basic MFC Classes : C string

Unit-II : Graphic Device Interface (GDI): C Client DC, C Window DC and C Paint DC classes, stock GDI project, color and

> Font, C Pallette, C Pen, C Rgn Classes. fonts, drawing shapes and curves, C Bitmap, C Brush, C

modeless dialogue. controls OK and cancel buttons, tab stops and groups, ComboBox classes, Data exchange to / from variables, and Dialog Box: C Dialog, C Edit, C Button, C List Box, C

Unit-III : C Status Bar. Ctrl, C Spin Button Ctrl, C List Ctrl, C Tree Ctrl classes, C file Windows Control and Dialogue: C Progress Ctrl, C Slider Dialg, C Color Dialog, C Font Dialog, C Print Dialog, classes Tool bar; tool tips, and status bar : C tool Bar, C Control Bar

edit view, C rich Edit view, C rich Edit Ctrl. Property Sheets Property Page class, MFC text Editing, C

extended combobox controls. Date time picker, month calender, IP Address Control

Exception Handling: C exception.

Unit-IV: Menus and Accelerators: Command Processing C Menu Cemd UI classes.

enabling, disabling menu items. Floating popup menus: Keyboard, accelerator, file menus

Unit-V : Multithreading: Multithreading Concepts: C Unit thread Advance Document Handling: C List view, C tree View, C mute X, C semaphore Event signaling, event object Cevent Thread Synchronisation, critical section: Critical section, C

template and C multi Doc Template. Document Templates: C doc. templates, C single Doc form View, C record view.

C frame Wnd, CMDI frame Wnd, C child Frame, CMDI child Central sensitive help. Wnd, C splitter Wnd, User defined message handling.

Unit-VI : DLL: MFC Extension DLL: Experting Classes MFC regular

containment and aggregation, daifference between Activeinterfaces, Zn-Process and out-process servers, marshaling, Control events. X and ordinary control. Properties, mapping Active-X Interface definition Language, Z Unknown, Z Class factory Basic Component Object Model (COM): Introduction

Case Study: Calender and Web browser controls ODBC DAO, OLE.

Programming Microsoft Visual C++, 5th edition, Kruglinsli, shephard wingo (Microsoft Press 98)

2MCS3

Unit addressing modes, interrupts previous intel mps. model of the pentium, registers, data, instructions Introduction to pentium, real mode, protected mode, software hardware/software requirements, PC, developing s/w for PC Introduction to Pentium Microprocessor: Microprocessor introduction, evolution of MP, block diagram, mp operation

Unit-II : data transfer, strings, arithmatic, logic, bit manipulation addressing modes, instruction examples: processor flags, **Instructions**: Introduction, ALP, pentium instructions program transfer, processor control.

Unit-III : Memory & I/O Interface: Memory devices, address interfacing, address decoding, PPI, Kbd I/f, display I/f, 8254 decoding, 8086 memory I/f, Pentium memory I/f, I/O PCI, ADC, DAC.

Unit-IV: hardware interrupts, PIC, RTC, Introduction to DMA> **Interrupts**: Introduction, basic interrupt processing

Unit-V : **Arithmatic Coprocessor:** 80 x 87 architecture, preliminary instructions, Bus I/f:

ISA, GISA, VESA, PCI.

Advance Programming with I/f: Programming with DOS and BIOS function, calls, kbd, video, speaker, printer control programming applications : Mouse, TSR, Interfacing c with & programming, command line interface, advance

- The Pentium Microprocessor: Antonakos Pearson
- The Intel Microprocessors Bary Brey Pearson
- 2664 Assembly Language Programming for PC - Socha & Norton (PHI)
- IBM PC Assembler Language Programming Peter Abel Pearson-
- 5 Essentials of Assembly Language Programming for the IBM PC Rajaraman, T., Radhakrishnan (PHI)
- 9 Fundamentals of Assembly Language Programming - Xefiner (GP)

COMPUTER GRAPHICS AND IMAGE PROCESSING

Unit-l : Geometry and Line Generation: Overview, pixel, and frame buffers, vector and character generation, displaying frame

Graphics Premitives: Overview & Introduction, display file, display control, text line style primitives.

Polygons: Representation, entering & filling polygons.

Unit-II : other transformations and display procedures Transformations: Scaling, Sin & Cos, Rotations, translation

display file structure. Segments: Operations on segment, Image transforms

Viewing: Transformation, implementation

Clipping: Various clipping Operations.

Unit-III : interactive techniques. **Interaction**: Input device handling, event handling

algorithms for hidden surfaces and lines shading and curves **3D Geometry:** Overview, transformations, projections

Unit-IV: techniques, two dimensional systems and methematical Image Processing: Overview of image processing preliminaries.

Image Reception: Introduction, visual system, color

Unit-V : Image Sampling and Quantisation: Introduction, Sampling theory, image quantisation.

Cosine and Sin transforms. Noncausal representation of Image Transform: Introduction, unitary transforms, DFT, Image, Spectral, facterisation, Image decomposition.

Unit-VI: Image Enhancement: Introduction, Poin Operations, ımage filters and restoration. Histogram modelling, Special operations, introduction to

Books:

- Computer Graphics A Programming Appraoch: Stevens Harington
- Computer Graphics 2nd Edition Hearn & Baker Pearson
- Fundamentals of Digital Image Processing: A.K.Jain Pearson
- Digital Images: A Practical Guide Greenberge & Greenberge (TMH)
- Understanding Digital Signal Processing: 2/e: Lynos (PE)
- Cooles The Essence of Computer Graphics Pearson. Digital Image Processing using MATLAB 1/e: Gonzalez (PE)

IMCS5 (1) ADVANCE COMPUTER NETWORK

Unit-I : models, Internet backbones, NAP & ISP, History. Introduction, overview, Network Core, medias, dalays,

Unit-II : **Application Layer:** Principles of Application Layer protocol, HTTP, FTP, e-mail in internet, DNS

Transport Layer: Services and Principles, multiplexing and demultiplexing applications, connection less transport: UDP,

principles of reliable data transfer, TCP, Introduction to congestion control.

Network I area & Pouting - Introduction and natural.

Unit-IV: Network Layer & Routing: Introduction and network service model, Routing Principles, Hierarchical Routing, IP, Introduction to Routing & Routers, IPV6.

Unit-V: Link Layer and LAN: Introduction, Services, Errors, MA
 P, LAN addresses and ARP, basics of ethernet, hubs, bridges, switches.

Concepts of IEEE 802.11, PPP, ATM, X.25, game relay.

Unit-VI: Multimedia Networking: Multimedia Networking Applications, Accessing Audio & Video Pro web serves, RTSP, RTP basics, Security in Computer Networks & Network Management.

B00KS:

- 1) Computer Networking: Kurose & Ross Pearson
- 2) Computer Networks & Internet: D.E.Comer, 4th Ed. Pearson

2MCS5 (2) MODELLING & SIMULATION

Unit-I: System Models and System Studies:-

Basic Concepts of Systems and System Modelling-Static and Dynamic/Physical and Mathematical Models-Principles used in Modellingcorporate Models-Analysis, Design and Postulation of Systems.

Unit-II: Basic Concepts and Continuous Systems:-

Techniques used-comparison of Analytical Methods and Simulation Numerical Techniques-Distributed log models and Cobweb Models-continuous system Models-Analytical Equations and Methods of obtaining solutions-analog and Hybrid Computers and Simulations Cssls Examples of diffeent Continuous Systems.

Unit-III: System Dynamics, Probability concepts and basic principles of Descrete Simulation :-

Growth and Decay Models and system dynamics diagrams examples Stochastic Process-probability functions and their evealuation-Random number generation-rejection method-comparison of Monte-Carlo method and Stochastic Simulation-examples.

: Queueing Theory, Inventory control and forecasting :Arrival patterns-service Time-different distributions
queueing discplines and measures grade of service
simulation of queues.

Unit-V: Descrete System Simulation and design and evaluation of simulation experiments:-

Discrete events-length of simulation runs and representation of time variance reduction techniques-experimental layout and Validation-generation of arrival patterns-examples statistical reports-utilisation and occupancy-choice of simulation language.

Unit-VI: Simulation Languages and Introduction of

GPSS:diffeent special purpose languages used for continous and discrete systems and comparison-factors affecting the selection of a discrete system simulation language-comparison of GPSS and Simscript. A detailed study of GPSS with examples.

ext books :-

- l) Groffrey Gordon, "System Simulation" Second Edition PHI
-) Narsingh Deo, "System Simulation with Digital Computers", PHI
-) Shannon R.E. "System Simulation: The Art and Science" Prentice Hall, Englewood Cliffs, NY.
- 4) Gordon, G. "The Application of GPSS v to Discrete System Simulation" Prentice Hall, Eglewood Cliffs NY.
- 5) Discrete Event System Simulation Banks, Carson, Nelson Pearson

2MCS5 (3) MULTIMEDIATECHNIQUES

Unit-I : Introduction: Multimedia overview, applications, goal and objectives, multimedia building blocks, multimedia and internet multimedia configuration.

Multimedia PC workstation components, multimedia platforms, multimedia development tools, authoring tool, interactivity, high end multimedia architectures.

- Unit-II: Multimedia O.S., File system (file format: TIFF, BMP, PCX, GIF etc.) process management, multimedia communication system, multimedia database management system.
- Unit-III: Multimedia Audio: Basic sound concepts, audio capture, music speech sound processor, sound recovery techniques, VOC4WAV file format for sound.
- Unit-IV: Multimedia Graphics: 2D/3D Animation, fundamental, digital imaging: Capture, animation, processing recovery, AVI file format, NTSC, PAL, GECAM, HPTV system, conferencing, streaming, motion synchronisation.
- Unit-V: Image Compression: LZW, DCT run length coding, JPEG

architecture, SGML, OOA. MPEG, hypertext, MHEG, hypermedia, document

mounted display. devices, VR chair, CCD, VCR, 3D sound system, head Augmented and Virtual reality & Multimedia: Concept, VR

devices, CDROM, DVD, Scanner. Multimedia Devices: Mass storage system; Magnetic

movies, music, midi controls.mm and Unix. function calls, windows support to sound, animation Windows Support : Multimedia database in oracle, mm

- Multimedia: Computing, Communication, Applications: Steinmetz
- 2 & 4 & Multimedia in Practice: Technology and Applications - Judith (PH)
 - Fund of Multimedia by DREW PEarson (Practical Approach)
 - Multimedia Comm. by Halsall Pearson
- Multimedia Buford Pearson.

2MCS5 (4) COMPUTATIONAL SCIENCE

Unit-I : Set Theory: Introduction, Set Elements, discriptive, types relations, functions, recursion. products, partition sets, minsets, algebra of sets and duality Venn-Euler diagram. Basic set operations, fundamental

Unit-II : Graph Theory: Introduction, notations, and definitions path and connectivity types, sub graphs, isomorphic graphs, representation, Eulerian and nonmiltonian graph, trees.

Unit-III : Algebric Structures: Introduction, operation, semigroup monoids, groups, subgroups, ring. Lattices: Introduction, types, pradicate calculus

propositional calculus, boolean algebra.

expressions. Finite Automata: DFA, NFA, equivalence, properties, regular

Unit-V : Context Free Lanagueges: CFG, PDA, PDA & CFG properties, parsing.

Unit-VI : examples, extensions, NTM. Uncomputability Turing Machine: Definition, computing, combining computational complexity.

- Discrete mathematics: J.K.Sharma (M)
- Elements of Theory & Computations Levvis & Papadimitron PHI
- 325 Introductory Theory of Computer Science - V.Krishnamurthy (EWP)

- Automata & Theoretical Computer Sc- Regade Pearson.
- Introduction to Automata Theory Hopcuft 2/e -- Pearson
- Discrete Mathematical Structures Kolman Pearson.
- Discrete Mathematics Johnsonbaugh Pearson

2MCS5 (5) COMPILER WRITING

Unit-I : Introduction to Compilers: Overview, Structure ımplementatıon.

regular grammers & expressions. grammers, derivation, reduction, syntax tree, ambiguity Programming Language Grammers: Inter Language

Unit-II : Scanning and Parsing Techniques: The Scaner, parser translation, elementary symbol table organisation

Unit-III : Memory Allocation: Static and dynamic memory allocation allocation, common & equivalence allocation. array allocation and access, allocation for strings, structure Introduction to Complition of expressions.

Unit-IV: Compilation of Control Structures: Control transfers constructs. procedural calls, conditional execution, interation control

Unit-V : Error detection, indication & recovery.

compilation of FORMAT list, IOSUB, file control Compilation of I/O Statements: Compilation of I/O list

Unit-VI: Code Optimisation: Major issues, optimising Global Optimisation, writing compilars. transformations, local optimisations, program flow analysis

- Compiler Construction D.M.Dhandhere (M)
- Compiler Writing Tremble-Sorenson (TMH)
- Computers: Princ, Techniques cools by Aho Person.
- The Essence of Compilers by Hanter Pearson

2MCS6 COMPUTERLAB.-III

Practicals based on subjects 8 and 10.

2MCS7 COMPUTER LAB.-IV

Practicals based on subjects 9, 11 and 12

"Distribution of Marks for Computer Lab.-I & Lab.-II

 $\stackrel{>}{\sim}$ Each student shall perform two practicals.

 \mathbf{B} Questions slip for each examinee, shall be based to anser book.

0 Marks should be given on the basis of the following break up.

III) Viva-Voce (Each practical 10 marks): II) Practical-II Practical-I 20 Marks 12 Marks 12 Marks

IV) Record Book

06 Marks

Total 50 Marks"

Syllabus prescribed for

M.Sc.Part-II Semester-III (Computer Science)

WEB TECHNOLOGIES AND PROGRAMMING

3MCS1

Unit copkies, progress indicator, automising browsers, handling and internet evolution, features of www, browsers, HTTP, computer networks, DNS, setting up internet, WWW: Web on society, services on internet, protocols, IP adresses web pages, browsing tricks. URL, Hypertext, search engine. IE & NN, Book marks, history, Internet: History, growth, architecture, applications, impact

FTP : Servers, clients, Telnet, IRC. E mails - mail servers and networks, protocols, clients

Unit-II on web, EC adaption, issues, applications, future. EC E-Commerce: Perspectives of E-commerce, framework practices, b2b, b2c, c2c,b2g, g2b,g2b, g2c benefits banking, EC for B2B & e-governance. information management, EC on private networks, EDI, EC imitations, EC payment, transactions, EC model, online

site on your, own computer, accessing PWS, publishing Web Servers: PWS, PWS setup, starting DNS, creating information preparing applications, dynamic application using databases, IIS, Apapche, Jigsaw, proxy servers.

games, meta tags, CSS. HTML4: Introduction, common tags, common tags, text styling, linking, images, lists, formating list, tables, forms

Unit-V : Javascript: Using JS, arithmatic, decision making, objects,

JDBC/ODBC introduction. sub-objects, methods, control structures, functions, arrays,

Unit-VI: authoring tools. accessing database for ASP. Introduction JSP, Web ASP: Introduction, working with ASP, client side scripting X components, file system objects, session tracking cookies server side scripting, simple ASP example, server side active

- IT Tools and Application (M)
- Bridge to Online Store Front Agrawala & Agrawala (M)
- Internet and Web Design (M)
- Internet and WWW How to programm Dietel & Dietel Pearson
- Developing E-Commerce Sites Sharma & Sharma Pearson
- Web 101: Lehnert Pearson
- Active Server Pages 3.0: N.Chare (Que)
- Frontiers of E-Commerce Kolkata & Whitson Pearson

3MCS2 ADVANCED DATABASE MANAGEMENT

Unit-I : Introduction to DBMS, Data Models, Transaction management, DBA, wers, E-R models, relational model.

Unit-II : Object Oriented Databases, data models, OO languages concurrency control, recovery system.

Unit-III: databases, distributed data bases. systems, parallel systems, distributed system), Parallel Database system architecture, (Centralised systems, cls

Unit-IV: & data visualization. Introduction to Data mining, were housing & visualization What can a Data warehouse do?, Foundations of Data mining

Unit-V : Data warehouse Introduction: Data warehouse architecture Metadata.

Unit-VI: Introduction to data mining, MOLAP, ROLAP, techniques used to mine the data, market basket analysis, current limitations and challenges to D.M., Data visualization.

Books:

- Database System Concepts S'lberschatz, Korth, Sudarshar (MacMillan)
- Database Systems by Connolly Pearson
- Fund of Data base systems by Elmasri Pearson. 4/e
- 4 Marakas (Pearson Edition) Modern Data Warehousing, mining and visualization - George
- Introduction to DBMS Atul Kohate (Pearson Edition)
- Database Management System Pote (M)

ال ھ Data Mining: Introductory and Advance Topics 1/e: Dunham (PE)

3MCS3 ADVANCE MICROPROCESSOR DEVICES

UnitI Overview of Microcomputer system, instruction prefetch wait state, interrupts & controller. peripheral devices. PC-overview, Hardware-BIOS-DOS interaction, mother board logic, memory & I/O addressing interrupts, I/O techniques, controllers & error detection,

UnitII .. Mother Board of IBMC PC:

logics, and I/F. Support chips: 8284, Bus controller, 8259, 8253, 8255A, 8237 Mother board functions, Reset logic, CPU logic, other

Unit-III : overview FDC. Overview, FDC, FDD, IC, Commands, 9216 Peripheral Controller: Introduction, Interface, Hardware

Unit-IV: **HDC**: Overview, interfacing, controller ports, commands design & types, display : CRT display, controller, adapter.

Unit-V : checks, configuration, upgradation, softwares, moventire PC Installation and Maintenance: Planning, installations maintaining, computer faults, diagnostic progress & hols.

Unit-VI: PC Troubleshooting: Bus faults, Symptons, diagnosis motherboard problems, peripheral problems, IC faults Rectification, POST, diagnostic softwares, checks,

- Pentium Microprocessors: Antonakos Pearson
 - IBM PC maintenanc; Troubleshooting- Govindrajalu THM.
 - Intel Microprocessors Bany Brey Pearson
 - Using A2-P Allan Wyatt.
- The 8088 & 8086 microprocessors Pearson Tribel

CLIENT-SERVER COMPUTING

JDBC: Overview, JDBC-ODBC bridge, Java SQL package creating C-S Application using JDBC oracle / Access and JDBC related classes, Architecture of JDBC application,

Unit-II : of class cookies, sersion tracking, C-S application using response, Httpget servlet, Http post & get, cookies, methods of class http servlet, Hipp servelet request, Http servlet Servelets: Methods of Interface servlets, Important methods

> Unit-III : uinte object exerution & running. C-S application using RMI definition, wether Item class definition, uniregistery running RMI: Temperature Server interface, class temperature server Impl., weather Into. class definition, temperature client class

Unit-IV: and server side, C-S application. Networking: Loading from URL, reading through URL server portion of C-S stream socket, demonstrating clien

Unit-V : Java beans: Windows of Bean Box, property, move curser between Explicit Butten and Juggler. resize cursor, selecting event, target selecter line, interaction

Unit-VI: Java beans Contd.: File dilog, other dilog, applet, applet jar, loading bean, animation, setting up event, class slider running, standalone application, contents of logoanimator field panel, selecting property.

- Java How to Program : Diellel & Dietel Pearsor
- Inside Servelets: D.R.Collaway Pearson
- Java 2 Complete Reference : Schidlt & Maughta (TMH)
- Using Java 2 Platform D.L. Webeu (PHI)

3MCS5(1) EMBEDED SYSTEMS

Unit-I : Multitasking, Embeded processors, Languages, Kernel Embeded Systems: Introduction, Design goals, real time, building, Embeded applications and proforms.

Unit-II : intel architecture, introduction to Microcontrollers and its Programmers View of Computer Organisation, overview of Binary Representation of Integers & real No. ACSII, BCD. Data Representations: Fixed Precision - Binary Numbers,

Unit-III : Using C: Integer data types, mixing data types, type structures, variant access. manipulating bytes in I/O ports, accessing I/O devices defination and define, manipulating bytes in memory

Unit-IV: procedure call and return, parameter passing, retrieving rise of addressing options, instruction sequencing Mixing C & Assembly: Programming in ALP, register usage parameters, pass by value, temporary variables. I/O Programming, interrupt I/O driver, DMA.

Unit-V : deadlocks, watchdog times. resources and critical section, scheduling: methods ground systems, multithreading, programming, shared Concurrent Software and Scheduling: Programmed/back

Memory Management and System: Initialization: Objects memory concept and its access. Introduction to system in C, Scope, lifetime, Automatic Allocation, Static Allocation, Difference Dynamic Allocation, Recursions using share initialisation

- Fundamentals of Embeded Softwares: Lewis Pearson
- An Embeded Software Primer Simon Pearson
- ω 8051 Microcontroller and Embeded System - Mazidi and Mazidi

3MCS5 (2) OBJECT ORIENTED TECHNOLOGY

classes, inheritance, reusability, new data types characteristics, characterisation of OO languages, object, Need for object Oriented Programming: Procedural polymorphism, and overloading. Languages, object oriented approach, advantages,

Object Oriented Design: Object Structure Concept, object types, attribute types, redation types, object behavioural

Unit-III : methodology, Chen and Chen methodology, design Methodology For Object Oriented Design: Booch modelling, system design, life cycle, model types, iteration hierarchy, packaging strategy, check point strategy.

Unit-IV: Overview of Object Oriented Programming: (C++/Java) arrays, pointers, inheritance, virtual functions. Loops, decision, structures, functions, objects and classes

Unit-V : oriented system. of OO databases, Gemstone / O2/Oerien distributed object Object Oriented Data bases: Relational Vs object oriented databases, the architecture of OO data bases, query language

Object management group, CORBA.

Unit-VI: Object Oriented Software Engineering: Object Oriented system, concept and management issues, OOA, object oriented design and testing, OO metrics, OMT technology

Books:

- Object Oriented Software Development McGregor and Kykes (Van Nosterdam)
- 2 & 4 & C++ Programming Language 3/e - Stroustrap
 - Object Oriented Programming in C++: Laffore (GP)
 - Object Oriented Programming in C++: (M)
- Object Oriented Programming using C++ 2/e by Pohl Pearson

- Bhave oop with C++ Pearson
- OOP with Ansh & Turbo C++ by Kamthane Pearson ****

3MCS5 (3) NETWORK SECURITY

- Unit-I : viruses, woury, trosan hasses, multilevel model of security Introduction: Terminology, Notation, Networking, Altasks, layers and cryptography, authorisation, tempest, keys legal issues.
- Unit-II : standards and modes & operation, Hashes and messages Cryptography: Introduction, breading on encrypti scheme, three hinds of cryptographic function, respective algorithms
- Unit-III: security hardshake pitfalls: login only, actual authentication, Authentication: Overview of authentication system integrity / encryption for data, mediated authentication protocols, keys, intermediatouries authentication of people performance consideration.
- Unit-IV: communication security. algorithms, messages, Introduction to Real time rights, Ticket lifetimes, Key versions, optimisations Standards: Kerbrose Vs: ASN.1, Names, Delegation of

IPSec, AH & ASP: Overview of IPSEC section, IP & IPV6

- Unit-V : security services for e-mail, Establishing Keys, Privacy E-mail Security: Distribution lists, store and forward containment other issues, PEM & MIME, PGP. Authentication of source, Message Integrity, Non repetation Proof of Submission & delivery, confidentiality, anonymity
- Unit-VI : Firewalls: Packet filters, application level gateways Encrypted tunnels, comparisons.

Security Systems: Network V4, Windows

Web Issues: URLS, HTTP, Cookies, other Web Security

Other Secutiry Measures

Books:

- Network Security: Kaufman, Perlman, Speciner-(PE)
- Network Security: Ankit Fadia (M)
- Network Security Essential by stallings Pearson
- Cryptography & Network Security by Stallings Pearson

3MCS5 (4) SOFTWARE TESTING

Testing: Introcution and Outline - Introduction to testing approach, outline approach steps, evaluation and schedule and test outline, sample application, incremental testing

shpreadsheets, sample applications, Documenting test documentation short cuts, introduction to using taples and Introduction to test outline to test cases, creating test cases,

Unit-III : Other types of tablets, slate machite, test care table with comparison system testing example, Unit testing of Classes data, managing tests, testing objectoriented software multiple inputs, decision tables, application with complete

Unit-IV: testing, post examination testing. testing, reliability and availability, security testing, database functional and sability issues, configuration and compalicity Testing Web Applications: Introduction, sample application,

Unit-V : schemes, trading selected test cases. guidelines, priority category, scheme, Risk analysis Reducing the No. of test cases: Introduction, prioritization interviewing to indentify problem neas, combination

Unit-VI: Creating Quality Software: Introduction, development software testing tools, applying software standards to test environmental infrastructure software testing environment, documentation

- Introducing Software Testing: Louise Tamres (PE)
- 29 Software Testing in the Real World by Kit - Pearson

3MCS5 (5) and Optimization Techniques Computer Oriented Numerical & Statistical Methods

elimination, Gauss Seidal, Gauss Jordan methods sub.method.Solution of Simultaneous equation; Gauss Iterative methods: Introduction, Roots of equations false position method, NR method, Interpolaion techniques. Transcedental equation & its solution, Bisection method, Direct

Numerical Differentiation, Numerical Integration, Solution of Differential equations

UNIT-III: Sampling, Frequency distribution, Measures of Control distribution, Numerical Characteristics. Binomial Distribution, Poisson distribution, Hypergeometric tendency & Dispersion, Moments, Discrete distribution,

Curve fitting: Linear least square fit, Nonlinear fit, Fitting of

UNIT-IV: Coefficient of correlation, Properties, Multiple, Partial & rank correlation.

Dynamic programming. Test of significance: Y² test, t test, F-Test. Introduction to

UNIT-V: Inter programming, Branch & Bound algorithm & Constraints, Minimisation, Simplex method, Transportation Linear programming, Formulation of models, Graphic soluion problem.

applications, Inventory models, Introduction to sequencing

UNIT-VI: strategies, Solution of 2 x 2 games, Brorron's Algorithm. Random variable concept, Polynomial & Simple regression, Decision theory, Game theory: Minimax-Maxmin pure Introduction to PERT, Introduction to quening theory.

BOOKS:-

- Computer oriented Numerical Methods V. Rajaraman(PHI)
- Computer Oriented Statistical & Numerical Methods E Balaguruswamy (M)
- Introduction to Operation research Gillett (TMH)
- Mathematical Statistics J.N. Kapoor(MCG)
- Statistics Murray R. Spiegel (MCG)
- Probability & Statistical for Engineers Irwin Miller John E. Ereund
- Operations Research by Natarajan Pearson
- Operations Research Taha Pearson
- Mathematical Statistics by Hogg Pearson

3MCS6 COMPUTERLAB.-V

Practicals based on subjects 1, 5.

3MCS7 COMPUTER LAB.-VI

Practicals based on subjects 2, 3 & 4.

Distribution of Marks for Computer Lab.-I & Lab.-II

- \triangleright Each student shall perform two practicals
- $\overline{\mathbb{B}}$ Questions slip for each examinee, shall be based to anser book
- Marks should be given on the basis of the following break up

II) Practical-II Practical-I 30 12 Marks 12 Marks

III) Viva-Voce (Each practical 10 marks): 06 Marks 20 Marks

IV) Record Book

Total 50 Marks

SEMESTER-IV

4MCS1 ARTIFICIAL INTELLIGENCE & EXPERT SYSTEM

UNITY: **Prolog Programming:**

structure of language, cut, fail, recursion, lists and complex expert system in prolog. structures, programming practice, interactive programming Introduction to turbo prolog, introduction to language,

Unit II Introduction:

solution steps, predictability, absolute and relative solutions production systems, control strategies, futuristic search problems and problem spaces, defining the problems recongnition, level of the model, criteria for success problem characteristics, decomposition of problems Definition of AI, AI techniques, tic-tac-toe, pattern

Unit-III : with variables, heuristic functions, weak methods, problem and graphs, knowledge representation, matching indexing Basic problem solving methods, resoning, problem trees analysis of search algorithms. reduction, constraints satisfaction, means-ends analysis

Unit-IV: secondary search, using book moves limitations. Games playing: Minimax search procedure, adding alphabeta cutoffs, additional refinements, waiting for quiescence

Unit-V common knowledge structures, choosing the level of representation, structural represention of knowledge: some represention, finding the right structure as needed Knowledge representation using predicate logic: representing declarative representation. simple facts in logic, augmenting the

Unit VI: Natural Language Understanding: Concept of analysis, understanding, language generation and maching understanding, keywork matching, synetactic and symantic

translation.

General concepts of implementation of ai systems Introduction to pattern recognisition.

single & multilayer networks-i engineering artificial neural n/w: introduction, learning Rule based systems, symantics of cfl, semantic n/w, frames frame kit. Application, introduction to knowledge

Books:

- Artifical Intelligence by Elaine Rich, Mcgrawhill Inc
- Artificial Intelligence and Expert Systems Jankiraman, Sarukesi
- Expert System: Theory and Practice Ermine (PHI)
- Turbo Prolog Nath (GP)
- List Programming Rajeo Sangal (TMH)
- Rule Based Expert System M. Sasikumar (Narosa)
- Artificial Intelligence Russell Pearson 1st Text Book
- Prolog: Prog. for A.I. by Bratko Pearson

Int. to Expert Syst. - Jackson - Pearson

- 10) Principles of AI - Nils Nilson
- A.I. by R.J. Winston. Pearson
- Prolog Programming and Applications Burnhan & Hall
- ES: Theory and Practice Ermine PHI

4MCS2 PARALLEL COMPUTER ARCHITECTURE

Introduction to Parallel Processing:

structures, Architectural classification schemes, parallel Processing Applications. Parallelism in uniprocessor systems, parallel computer

arthmetic pipelines, Principles of Designing pipelined Pipe lining: An overlapped parallelism, Instruction and procecessors, Vector Processing Requirements.

Pipeline computers and Vectorization Methods:

and special features, vectorization and optimization methods language features in vector processing, Design of vector vector loops, The architecture of Cyber-205, Vector The architecture of cray-1, cray-2, pipeline Chaining and Scientific Attached Processors, Recent Vector Processors Evaluation of pipelined operations computers operations, Optimization of vector operations, Performance processing in Cyber-205 and CDC-NASF, Fugistu VP-200 The space of pipelined computers, Early vector Processors

SIMD Array Processors, SIMD Interconnection Network Parallel Algorithm for Array Processors, Associative Array

and control, Image processing on the MPP, Performance and the BSP Systems, The Massively parallel processor, processors, Multiple-SIMD computer organization. processing languages, performance Analysis of Array Processing, The space of SIMD computers, The Illiac-IV Enhancement methods, parallel memory Allocation, Array The MPP system Architecture, processing Array, Memory

operating systems, Exploiting concurrency for switch and Multiport Memories, Multistage Networks System deadlocks and prtection scheduling strategies multiprocessing, Interprocess communication mechanisms, Loosely coupled Multiprocessors, Tightly coupled parallel Algorithms. parallel algorithms for multiprocessors, performance of networks, Parallel memory organization, Multiprocessor formultiprocessors, Performance of interconnection multiprocessing, Time shared of common Buses, Crossbar Multiprocessors, Processor characteristics for

Example Multiprocessor Systems:

commercial multiprocessors, The C.mmp multiprocessor Operating system, Performance of the C.mmp, The S-1 system. The C.mmp multiprocessor Architecture, The hydra Multiprocessors Univac 1100/80 and 1100/90 series, the Architecture process, Execution Modules, Parallel development, The HEP Multiprocessors, The HEP system Multitasking on Cray X-MP, performance of Cray X-MP. IBM 370/168MP, 3033 and 3081, Operating system for IBM Processing on the HEP, Mainframe Multiprocessor systems, Multiprocessing uniprocessors. The S-1 software Multiprocessor, The S-1 System Architecture The space of multiprocessor systems, Exploratory systems Fandom Nonstop System, Cray X-MP System Architecture

UNIT-VI:

computer Architectures, Static Data flow computers Algorithms, Matrix Arithmetic pipelines, Real-time Image processors, VLSI Arthemetic Modules, Partitioned Matrix Alternatives, VLSI Computing structures, The systolic Array processing. Reconfigurable Processor Array, VLSI Matrix Arithmatic Architecture, Mapping Algorithm into systolic Arrays languages, Advantages and potential probelms, data-flow Data flow computers, VLSI computations and Neural Dynamic Data flow computers, Data Flow Design flow versus Data flow Computers, Data flow graph and Networks, Data-Driven Computing & languages, Control-

> brain in the computer. Introduction to Neural networks, capabilitities of human

TEXT BOOKS:

- Computer Architecture and parallel processing by Kai Hwang & Faye A Briggs (McGraw Hill.)
- Fundamental of Parallel Processing Jordan, Alaghband (PE)
- Parallel and Distributed Programming Using C++ Hughes, Hughes
- Introduction to Parallel Processing M.Sasikumar (PHI)
- Parallel Computing Ghoshal (UP)
- J.M.Crichlow (EEE) An Introduction to Distributed and Parallel Computing

REFERENCES:

- Computer Architecture and Organisation 6th Edition, W.Stallings
- Computer System Organisation and Archaitecture J.D.Carpinelli
- Computer Architecture and Design P.Pal Choudhari (PHI)
- Parallel Computer Architecture and Programming Rajaraman Murthy
- Advanced Computer Architecture Sima, Fountain, Kacsule (AW)
- Advanced Computer Architecture Kai Hwang (TMH)
- McGraw Hill. Design Efficient Algorithms for parallel computers- by Quinn
- Principles of Parallel an Multiprocessing by Descrochers McGraw
- Parallel Computing: Methods, Algorithms & Applications, 1989 by Evans Academic Publishers.
- <u>5</u> VLSI Risc Architecture and Organization - by Further Academic publishers.
- \exists Computer and Information Sciences current Trends in Applications - Editors V.B. Kaujalgi, Computer Society of India Tata McGraw
- 12) An introduction to distributed and parallel computing - Crichlow
- 14) 13) Elemants of parallel programming - Rajaraman - PHI
- Practical parallel programming Wilson PHI
- An Int. to Parallel Computing Grama Pearson

4MCS 3 (1) OPERATING SYSTEM DESIGN

UNITI : Introduction, The H/W interface, OS interface, design Techniques-I (for Unix & Win NT)

UNITH: systems, design Techniques III (For UNIX & WIN NT) Memory management, Virtual memory, Virtual memory

UNITIV: I/O devices, IO systems, file systems, file system organisation. Design techniques IV. (For UNIX & WINNT), Introduction to resource management & client server.

S/W, mouse, keyboard, video configurations, networks & file system, desktop, applets, DDE, OLE exploiting H/W & Windows 95 anatomy, Architecture, memory management,

UNITY: CASE STUDY: UNIX

programming, system administration features of LINUX & file syste, shell, vi editor, file attributes filters, mail, Shell Understanding Unix commands, Utilities (General purpose),

BOOKS:

- Operating system: Design oriented approach: Charles Crowley -
- 7 Peter Norton's complete Guide to WIN 95: Peter Norton, John Muller
- ω UNIX concept & applications : Das - TMH

REFERENCES:

- Design of UNIX OS Bach Pearson
- Modern O.S. Tanenbaum Pearson
- Unix Sanitabh Das PHI
- 200400 Unix programming environment - Kerninghan - Pearson
 - C & Unix programming Kutti TMH
 - Unix Programming on 80286/386 Deilmah BPB
- Linux the complete reference R. Peterson TMH

4MCS3 MOBILE COMMUNICATIONS

Unit-I : signal propogation, introduction to multiplexing simplified reference model. Frequencies, signals, antenas Mobile Communication: Applications, history, market modullation, spread spectrum concept, cellular system.

Medium Access Control: Introduction, SDMA, FDMA TETRA, UMTS & IMT-2000. Telecommunication system : Introduction to GSM & DECT TDMA, CDMA, comparison of S/T/F/CDMA

Unit-III : Satellite Systems: History applications, basics, routing localisation, hardware, examples.

digital audio & video broadcasting, convergence **Broadcast Systems:** Overview, cyclical repetation of data

Unit-IV: infrastructure and adhoc network, IEEE 802.11, HIPERLAN Wireless LAN: Intrared versus Ratio Transmission, Bluefooth.

Unit-V : adhoc networks. Layers: Mobile Network Layer: Mibole If, protocol, mobile

Mobile Transport Layer: Traditional TCP, improvements 2

Unit-VI: Support for mobility: Introduction to files systems, www. SG/3 G network WAP, i-mode.

1) Mobile Communication: Jochen Schiller (PE)

4MCS3 **OBJECT ORIENTED MODELLING AND DESIGN**

Unit-I : modelling, event states, operations, concurrency. Grouping concepts, agregation abstract classes, anociation, Generalisation and specialisation, Inheritance, thinking - rethinking, objects and classes, links and Review of Object Modelling: New paradigm, object oriented polymorphism, metadata, constraints, reuse, dynamic

Unit-II : of CRC card method. Booch Methodology, use CASE drive approach, overview Importance of Modelling: Brief overview of objects, OMP

Unit-III : software development life cycle, UML diagrams. of UML, architecture - Metamodel, mechanisms, unified OMG approval & UML:, scope of UML, conceptual model Overview of UML: Efforts of Standardisation, integration

Unit-IV: modeling architectural view. relationship, interface types and rules, packages common UML Diagrams: Advance Class Diagrams: Advnace modeling techniques, modeling groups and elements

Unit-V : colaborations, use cases, interaction diagrams, state transition diagram. prototypical instances, links, objects interactions Instances and object diagrams: Modeling concrete.

Diagram, pattern and framework Architectural Modeling: Component Diagram, Development

distributed objects systems: COM, DCOM, CORBA, object Introduction to Component Technology, concepts of oriented data bases

- UML users guide: Booch / Ram bough Pearson
- Object Oriented Modelling & Design R umfaugh Pearson
- UML in a multishell Ram boough (PHI)

DECISION SUPPORT SYSTEMS

Unit-I : Decision Making and Computerised Suport: An Overciew-

supply chain Management, hybrid support systems ANN, knowledge management systems, supporting ERP and concept, GSS, EZS, Expert System and intelligent agents Decision making, systems, modelling and support. Introduction, Managerial Issues, the need, Game work

capabilities, components, Data Management Systems DSS: Introduction, DSS configurations, characteristics and housing, Accers, Analysis, mining and visualisation, DSS DSS hardware, DSS & MIS, classifications. Detaware Management Systems, User Interface sub systems, the user Model Management Systems, knowledge Broad

Collaboration, Communication, Enterprise DSS and knowledge management:

support, GSS & Technologies, GSS meeting procers GSS: Introduction, communication support, collaboration Distance Learning, other issues.

Characteristics and capabilities. Enterprise DSS: Introduction, Evolution, Role and needs

Knowledge Management: Development, Methods, Success Tools, Technologies, Al, other issues.

examples, problem areas, benefits, limitations, success factor & types, internet, intranet and web. decision support: Introduction, types, ES, working of ES Fundamentals of Intelligent Systems: Knowledge based

system developments. knowledge presentation, inference Techniques, intelligen Introduction to: Knowledge acquisition and validation

Unit-V example, benefits and limitations, NN & ES, NN for DSS. algorithms, tooling, implementation, software and hardware, application development, data collection and preparations, Advanced Intelligent Systems: Neural Computing Al systems & applications, intelligent software agents. Neural Network: Architecture, preperation, training Introduction to: Neural computing Application, advanced Introduction, ML, NC, biology analogy, fundamentals

> Unit-VI : Implementation, integration and Impacts:

integration, intelligent DSS. ES & DSS integration, integrating EIS, DSS & ES and global Implementation: Introduction, major issues, strategies.

Impact of MIS.

1) Decision Support Systems and Intelligent Systems: Turban

4MCS3 ROBOTICS AND COMPUTER VISION

UNITH: representation structures of Robots, Numerical controls of machine applications, Nonindustrial applications, Basic Basic concepts in Robotics, Advantages and tools, Resolution, Accuracy and Repeatability, Position

continuous-path Robot systems. Robots, Articulate Robots, Direct and indirect drives, Cylindrical coordinate Robote, Spherical coordinate loops of Robotic systems, Cartesian coordinate Robots, Point-to-point and continuous-path systems, contro The wrist motion and the Gripper, structure of

systems, Mechanical transmission systems. of stationary position Errors, Control loop of CNC amplifier, control loop using voltage amplifier, Elimination approaches of Robots, control loops using current Hydraulic systems, Direct-current servomotors, contro

convention, Application of the DH method, Quaternion & Rotation vector Representations. Transformations using matrices, Denavit-Hartenberg Direct Kinematic Analysis, Coordinate & vector Direct kinematics problem in Robotics, Geometry-based

UNIT-III: of Interpolators, The solvability of the inverse kinematic commands, The tragectories planning, Basic structure problem, Particular solutions for the Inverse kinematics Necessity of Interpolators, the generation of Motion problem using Rotation vectors.

operating task programs, CAD for Robotics. controlled industrial Robots by training, storing and programming with graphics, programming of serve operational peripherals, Maximum effort manipulators, Implicit programming by training, Manual teaching, Lead methods for programming a programmable controller through teaching, programming languages for Robots Energy sources, Effect of gravity vibration problems

UNIT-VI: Prospects for knowledgebase Robots, Robot and expert systems, future prospects for knowledge-based systems and knowledgebased languages, production rule Artificial intelligence - parallel developments, Experts

Economic analysis, A case study, Robot safty Installation of a Robot, a plant servey, selecting a Robot,

UNIT-V:

detection, Measurement of distance, Recognition of Sensor and Intelligent Robots, Environment - Robot proximity sensor, Infrared proximity detection, object interaction in automatic gripping as monitored by

by a Robot, Cameras for industrial robots, choice of recognition as applied to Robots, image processing, systems information, optical illusions and image comprehension reconstitution, Image sensors, Representation of visual Positioning of visual sensors, sensor and depth Vision systems, Artificial vision to Robot, Pattern

UNIT-VI: **Application of Robots:**

of the production Rate, Welding, Spot welding, Arc several single machine tool, several machines, The robots, Task programming, Features of Assembly Robots welding, spray painting, features of spray painting manufacturing cell. The cellular concept, Optimatization Handling, loading and unloading single - machine tool Design for Automatic assembly, Drilling, Deburring metal

BOOKS:-

- Introduction to Robotics J.J.Craig (AW)
- Robotics for Engineers, 1987 by Yoram Koren (McGraw Hill)

REFERENCES:

- Robotic Engineering & Integrated Approach Klafter, Chmielewski, Negin (PHI)
- Robotic Technology Volume-I: Modelling & Control P.Coiflets -
- Ċ Robotics Revolution - Peter B.Scott
- Robot Technology Volume 4, Robot Components and Systems by Francois Lhote (Kogan Page Ltd., London NI) [Unit I & II]
- S Robot Technology Volume 5, Logic & Programming by Michel Parent and Clande Laurgeals (Kogan page Ltd., London NI) [Unit 3]
- 6. Robot Technology Volum 6, Decision & Intelligence by Igor Aleksander, Henri Farreny & Malik Ghallib (Kogan Page Ltd., London NI) [Unit 4]

- Robot Technology Volume 5, Interaction with the Environment by Philippe Coiffet (Kogan Page Ltd., London NI) [Unit 5]
- Int.to Robotics Niku Pearson
- Decision Support Syst & Intelligent Supt. 6/e Turban Pearson

4MCS 4 Practicals based on 1, 2 & 3. COMPUTER LAB-VII

Distribution of Marks for Computer Lab.-I & Lab.-II

 $\stackrel{>}{\leq}$ Each student shall perform two practicals

 \mathbb{B} Questions slip for each examinee, shall be based to anser book

Marks should be given on the basis of the following break up

II) Practical-II Practical-I 12 Marks 12 Marks

III) Viva-Voce (Each practical 10 marks): 20 Marks

IV) Record Book

06 Marks

Total 50 Marks

4MCS V & VI PROJECT/SEMINAR:

form\ Hard Copy (One for Guide & One for Department Library). feature should be included. Report be submitted in two copies in CD Should be selected on most current topic. Most of the advance

Education tour / industrial visits may be organised time to time and as per need.)

a) Distribution of Marks For Project/Seminar:-

- A) Each student has to submit project report he has under taken neatly typed / handwritten.
- B) Each student has to demonstrate his project to the examiner and has to face Viva-voce from which marks be allotted as follows:-
- A) Internal performance : Regularity 25
 (Internal Examiner) Performance 25
 Job Work 25
 b) External performance : Contents 25
 (External Examiner) Viva-voce 25

Total	
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150	

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Demonstration

					b)
Total	Reference/Topic Section/Literature Survey -	Viva-voce	Presentation / Delivery	Contents	For Seminar:-(By Internal Examiner Only)
I	ı	ł	ł	ł	
50	15	15	10	10	

NUEX

M.Sc. Semester I to IV Examination in Computer Science (Prospectus No.20101216)

Sr.No.	Subject Code	Paper/Practical	Page Nos.
	1	Special Note	<u>-</u>
2.	1	Ordinance No.4 of 2008	ω
		Direction No. 26 & 27 of 2010 M.Sc.Part-I Semester-I	
4.	IMCS1	Digital System & Microprocessor	9
5.	1MCS2	Data Structure & Algorithm (C++)	10
6.	1MCS3	Object Oriented Programming	11
7.	1MCS4	Digital Communication & Networking	12
œ	1MCS5	System Analysis, Design &	12
		Software Engineering	
		M.Sc.Semester-I Practical	
9.	1MCS6	Computer Lab - I	13
10.	1MCS7	Computer Lab-II	13
		M.Sc.Part-I Semester-II	
11.	2MCS1	System Software & Operating System	14
12.	2MCS2	Windows Programming	14
13.	2MCS3	Microprocessor Programme & Interfacing	16
14.	2MCS4	Computer Graphics & Image Processing	17
15.	2MCS5	Electvie-I	
		 Advanced Computer Network 	17
		2. Modelling & Simulation	18
		3. Multimedia Techniques	19
		4. Computational Science	20
		5. Compiler Writing	21
		M.Sc.Semester-II Practical	
16.	2MCS6	Computer Lab-I	21
17.	2MCS7	Computer Lab-II	21
		M.Sc.Part-II Semester-III	
18.	3MCS1	Web Technique & Programming	B

10	3MCc)	
19.	3MCS2	Advance Database Management
20.	3MCS3	Advance Microprocessor Devices
21.	3MCS4	Client Server Computing
22.	3MCS5	Elective-II 1. Embeded Systems
		3. Network Security
		4. Software Testing
		5. Computer Oriented Numerical &
		Statistical Methods & Optimiziation Techniques
		M.Sc.Semester-III Practical
23.	3MCS6	Computer Lab-I
24.	3MCS7	Computer Lab-II
		M.Sc.Part-II Semester-IV
25.	4MCS1	Artificial Intelligence & Expect
		System Design
26.	4MCS2	Parallel Computer Architecture
27.	4MCS3	Specialisations -
		 Operating System Design
		2. Mobile Communications
		3. Object Oriented Modelling
		& Design
		4. Decision Support Systems
		5. Robotics and Computer Vision
		M.Sc.Semester-IV Practical
28.	4MCS4	Computer Lab-I
		M.ScII Semester-IV Project/Seminar
29.	4MCS5	Project
30.	ANCE	Seminar
	4IVIC 30	